INTEGRATED CIRCUIT MEMORY DEVICES HAVING CLOCK SIGNAL ARBITRATION CIRCUITS THEREIN AND METHODS OF PERFORMING CLOCK SIGNAL ARBITRATION

Abstract of the Disclosure

A clock signal arbitration method includes arbitrating between first and second request signals generated in respective first and second clock domains that are asynchronously timed relative to each other, to obtain first arbitration results. These first arbitration results identify a relative queue

5   priority between the first and second request signals. Additional steps are performed to transfer the first arbitration results into a third clock domain that is asynchronously timed relative to the first and second clock domains. The transfer operation may include arbitrating the first arbitration results in a third clock domain to obtain second arbitration results that confirm or

10   correct the first arbitration results.

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